

FIG. 1

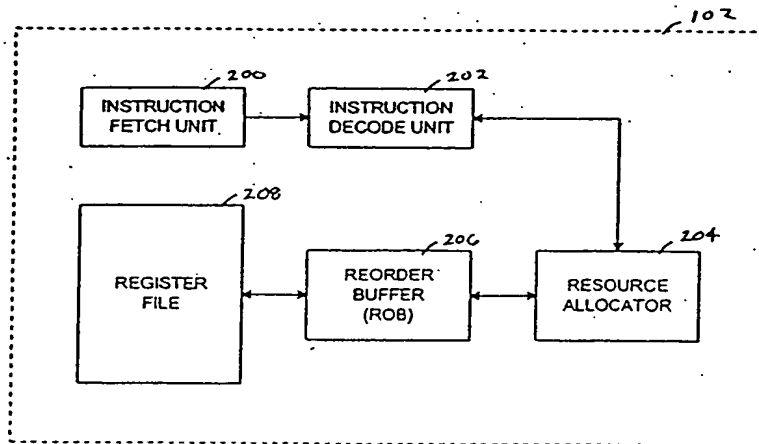


FIG. 2

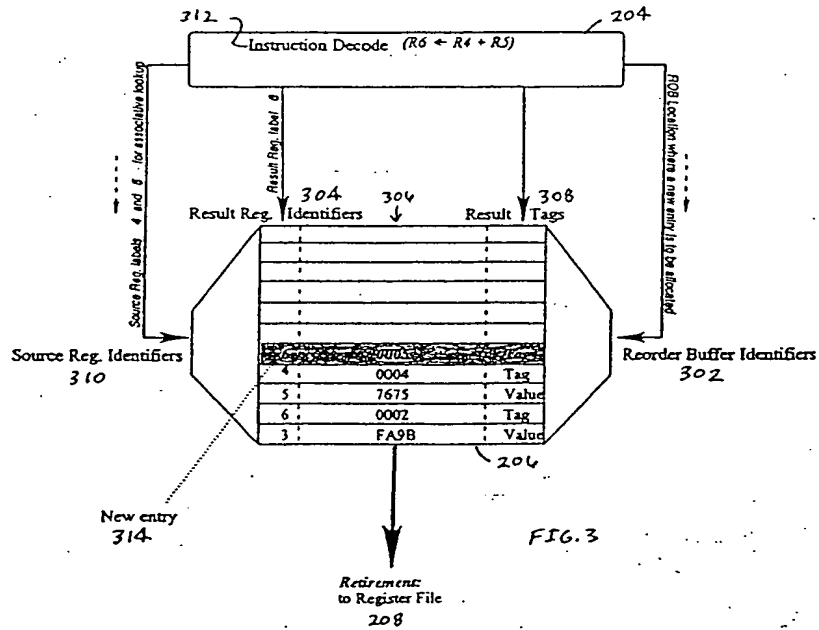


FIG. 3

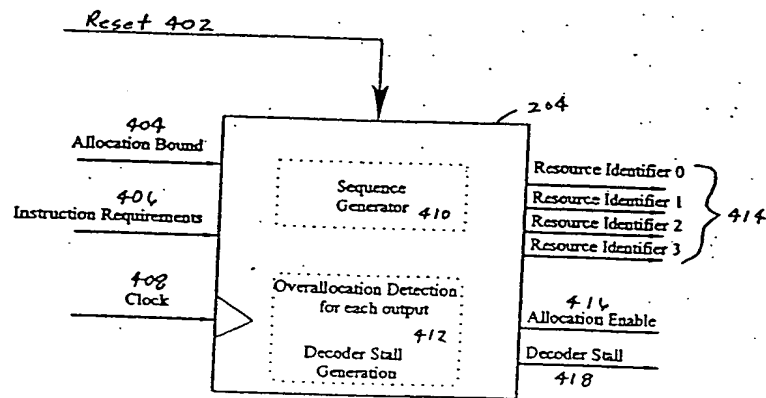


FIG. 4a

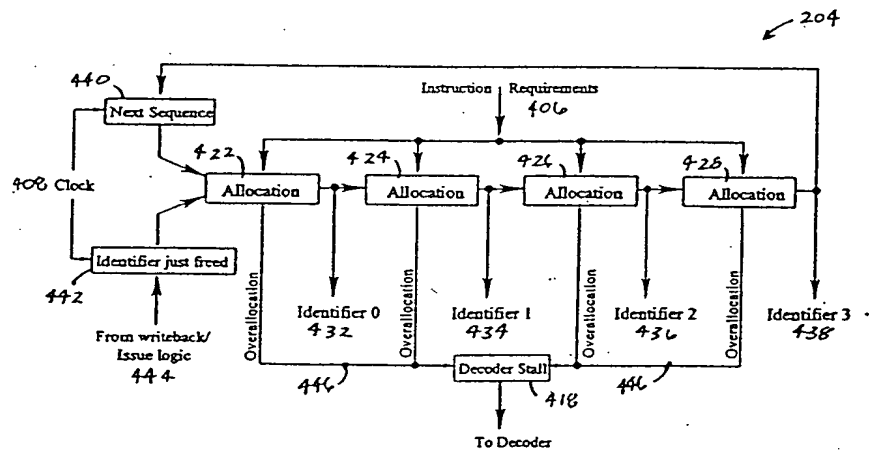


FIG. 4b

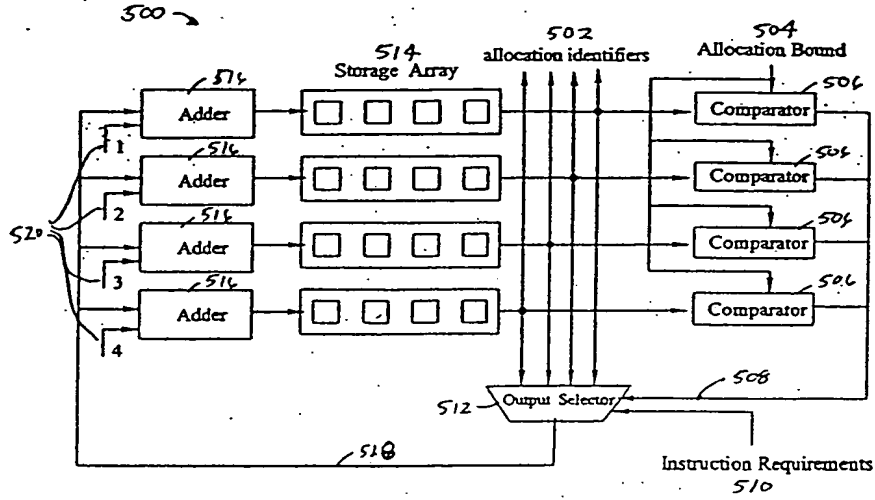


FIG. 5

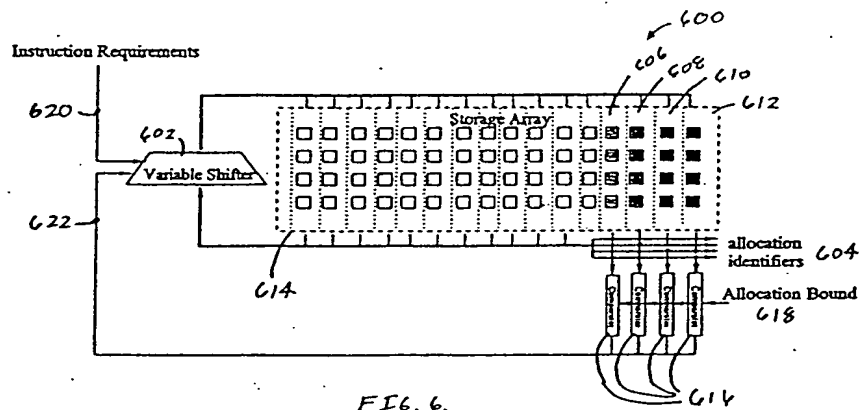


FIG. 6.

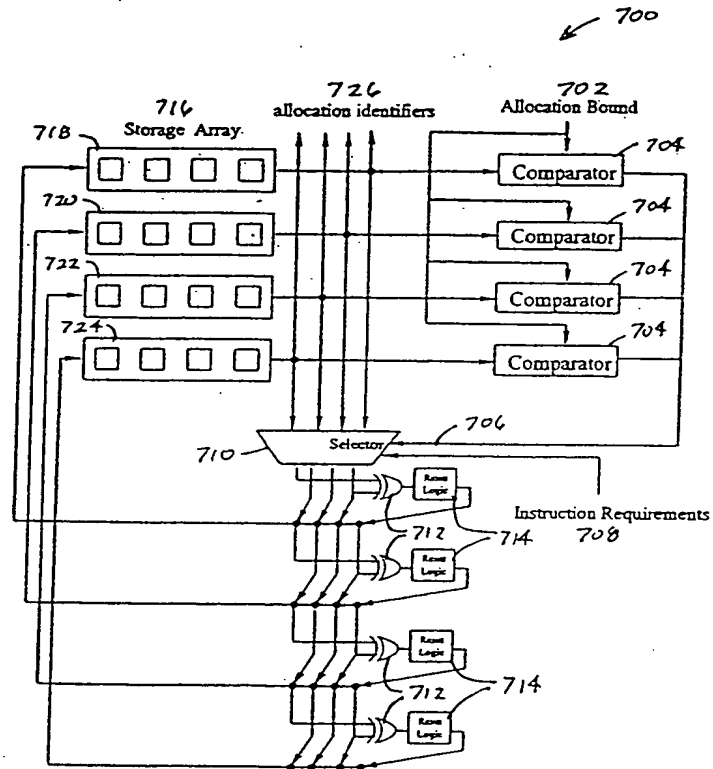
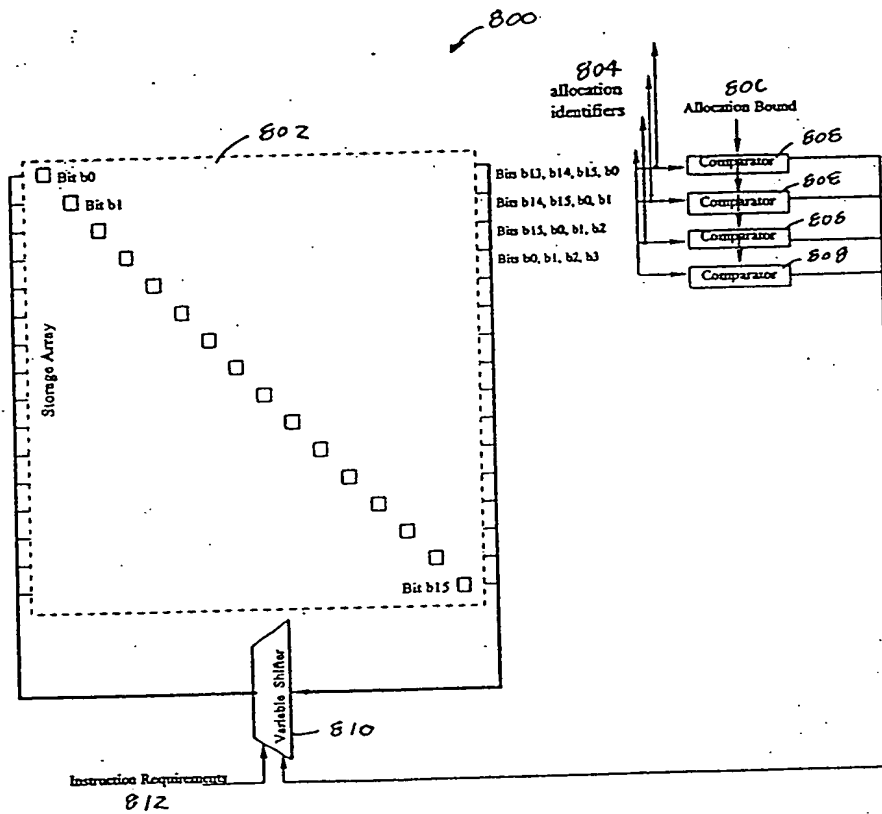


FIG. 7



FI 6.8

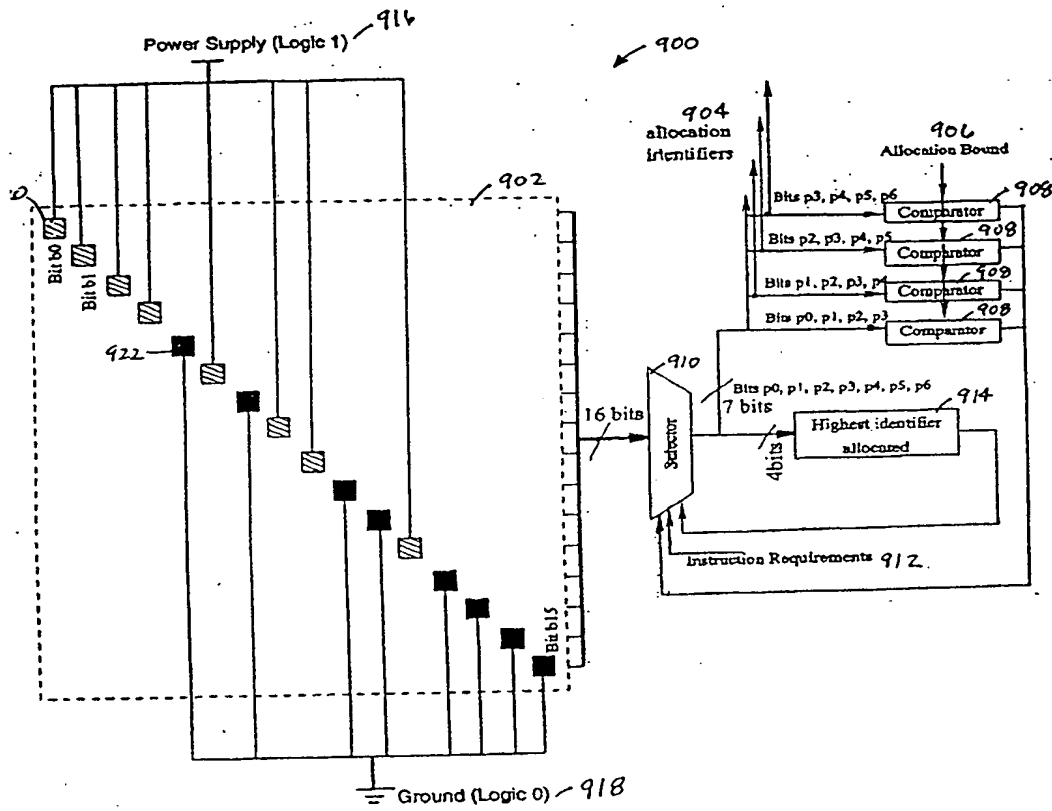


FIG. 9

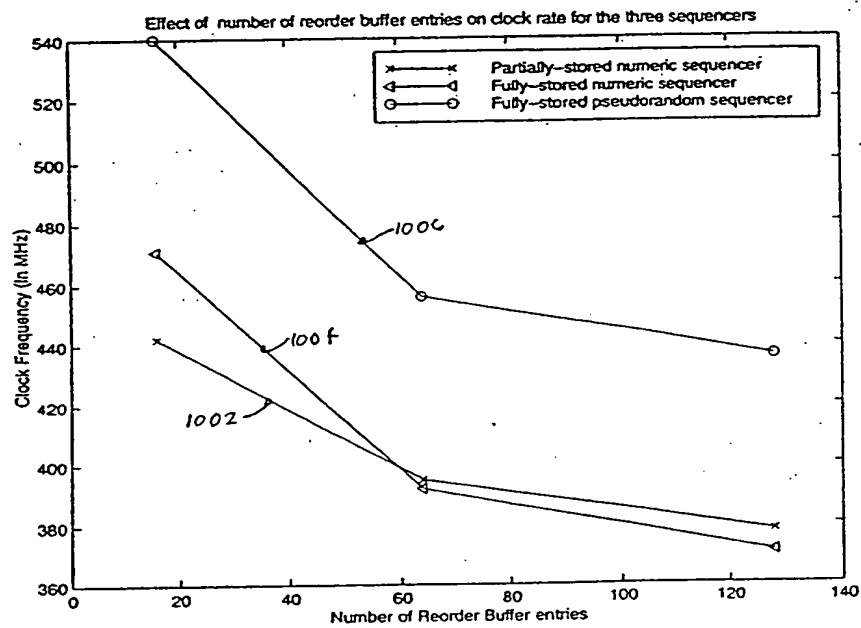


Figure 10: Effect of number of reorder buffer entries on clock rate for the three sequencers